

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	102	717/150.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L2	165	717/160.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L3	234	1 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L4	1	3 and cache near (synonym or alias\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:19
S1	427	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:17
S2	7080	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S3	2008	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S4	638	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S5	56	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S6	28	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S7	10	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42

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S8	4056428	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S9	166	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S10	4	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S11	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S12	9	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S13	35	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S14	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S15	325	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S16	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S17	136	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S18	14	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S19	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/01/29 13:47

EAST Search History

S42	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/04 14:30
S43	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/04 14:30
S44	0	"09416436".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:43
S45	464	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S46	7681	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S47	2142	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S48	691	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S49	61	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S50	32	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S51	12	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S52	4275424	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S53	182	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44

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S54	4	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S55	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S56	11	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S57	39	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S58	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S59	362	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S60	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S61	154	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S62	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S63	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S64	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S65	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44

EAST Search History

S66	496	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S67	8219	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S68	2287	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S69	740	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S70	67	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S71	36	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S72	14	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S73	4483130	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S74	200	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S75	5	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S76	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S77	13	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

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S78	45	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S79	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S80	393	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S81	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S82	168	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S83	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S84	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S85	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S86	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S87	0	"09416436".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S88	496	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S89	8219	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

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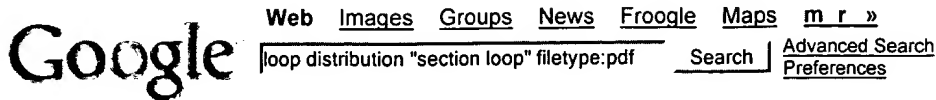
S90	2287	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S91	740	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S92	67	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S93	14	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S94	4483130	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S95	200	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S96	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S97	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S98	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S99	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S100	36	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S101	5	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

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S10 2	13	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 3	45	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 4	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 5	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 6	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 7	168	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 8	393	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 9	336	detail adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:16
S11 3	1048	loop near distribut\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:17
S11 5	272	S113 and (compiler or optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:17
S11 6	64	S113 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:59
S11 7	54	S115 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:42

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S11 8	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:27
S12 0	1	"6029225".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:32
S12 1	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:53
S12 2	853	strip adj min\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 3	1	S122 and S109	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 4	10	S122 and S113	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 5	46	S122 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 6	48	S122 and cach\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55
S12 7	1	S126 and synonym	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55
S12 8	6	S126 and (synonym or alias\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55

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slightly crushed and bent, flat-backed with hollow. **D-s ction l p ... Un ven distribution** of lead makes it. difficult to give accurate figures for the ...
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a parallel **section. Loop j** can be exchanged with **loop i** after the **loop distribution**. Then the compiler can parallelize the whole program as shown in Fig. 3. ...
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lo p distribution in this manner. First, **loop distribution** may be impossible ... As described at the outset of this **section**, **loop**-splitting (or iteration re ...
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tive cooling of a constant cross **section loop**, since over this narrow ... differential emission measure **distribution** of one flare with the ...
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label **distribution** protocol, by which one LSR informs another of ... **Section "Loop Detection"** specifies situations when an LSR must ...
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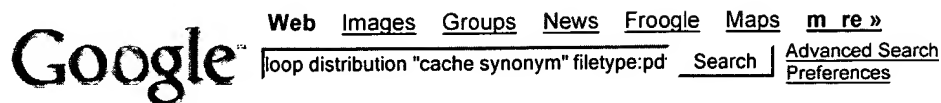
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and maximal values of the index **loop** and the fourth. parameter is an offset added to the index **loop** ... the animated crayons in the **distribution**. Thanks to ...
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Mining the web for **synonyms**: PMI-IR versus LSA on TOEFL. In: ...
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 called the ARP **cach** , is used to maintain a corr lation between each MAC address
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synonym (or aliasing) problem, sinc a **cach** lookup indexes into only one of the ...
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simplest one: time, which is often more accurately denoted by **synonyms** like ... **strip mining** once again, the same reasons for doing so as in the matrix ...
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cache line. The compiler directive PREFETCH enables a data prefetch from ...
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of **strip** pit topographically high in Mrak **mine**. ... 60% ----- From **Cache** Creek, 1.5 miles above **Cache** Creek. **Mining** Company's camp. Talkeetna (B-2) ...
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Blakeburn **strip mine** between 1954 and 1957. This operation mined the ... geyserite material (geyserite is a **synonym** of siliceous sinter). A ...www.em.gov.bc.ca/DL/MFReports/mines2001.pdf - [Similar pages](#) - [Filter](#)**[PDF] DEPOSIT INFORMATION MANUAL & DATA DICTIONARY**

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1924 **Strip mining** begins at Colstrip. 1929 Linderman begins PLENTY COUPS ... In the end, they ride off to the **cache** on the Musselshell River. ...
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Strip-mining is no longer appropriate with modern compilers; the loop can be ... Many of these options have equivalent **synonyms**, such as -autopar and ...
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microarchitecture bottlenecks such as **cache** misses and mispredicted branches. Clock ... following examples demonstrate **strip mining** and cleaning up loops. ...
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compiler-based principles: orthogonality, composability, etc. • RISC vs. CISC ... **strip mining**. • timing for vector instructions. • chaining and convoys ...
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formations such as adaptive **strip-mining** [Moritz et al. 1999], our approach ... Other hardware-based techniques aimed at dealing with the **synonym** prob- ...
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... program transformations such as adaptive **strip-** **mining** [Moritz et al. ... Other hardware based techniques to deal with the **synonym** problem are based ...
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Fortran **compiler**, later inherited by the Compaq and HP Fortran ... process, performs **strip mining** and loop optimizations, vectorizes data motion, ...
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The **synonym** of -ansi_alias is -assume [no]dummy_aliases. Alignment Options ...
strip mining. An optimization in which the **compiler** creates an additional ...
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Syn nym: abseil. See also. abseil; carabiner. rappel rack. A long U-shaped steel bar ... effects such as **strip mining**. recorder. An instrument designed to ...
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Synonym for a sump, or a section. of flooded cave passage, in common. parlance. ... surface mine. **Strip mine**. [16] . surface retention. Water held on land ...
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Synonym: `/unroll[:n]`. - unrolln. Specifies the maximum number of times you want to... **strip mining**. An optimization in which the **compiler** creates an ...www.ccs.usherbrooke.ca/downloads/compilateurs/Intel_V9_optaps_for.pdf[PDF] 1.0 GLOSSARYFile Format: PDF/Adobe Acrobat - [View as HTML](#)ling the overburden material of **strip mine** banks. The moving of soil particles closertogether by ... the static level (**synonym** of groundwater surface) of ...www.luscar.com/pdf/bowcity_glossary.pdf[PDF] The Role of APL and J in High-performance ComputationFile Format: PDF/Adobe Acrobat - [View as HTML](#)metric could allow a **compiler** to produce appropriate code for its manipulation. ...torization, parallelization, and **strip mining**. IBM's ...www.snakeisland.com/aplhiper.pdf[PDF] LNAI 2700 - Terminology Mining

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tuted by one of their **synonyms** without modifying the syntactic structure. ... e, weshould **strip** this ending from it and append the string e to the stem. ...www.springerlink.com/index/JK4KBK2YLP00KWL0V.pdf[PDF] Mining Component Behavior to Support Software Retrieval

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synonym for semiannual), biaxial, bibliographic retrieval system ... **strip-chart**recorder. **strip-mine** (v); **strip mine** (n, adj). **strip miner** ...mydocs.epri.com/docs/CorporateDocuments/WorkingWithEPRI/Terminology.pdf[PDF] Some Industrial Mineral Deposit Models: Descriptive Deposit MoFile Format: PDF/Adobe Acrobat - [View as HTML](#)Deposit **synonyms**: Marine evaporites, altered marine and rift-valley evaporites. ...Maximum limitation of overburden: Most gypsum is mined by **strip mining** ...pubs.usgs.gov/of/1991/ofr-91-0011-a/ofr-91-0011a.pdf[PDF] PCS 5702 – Arquitetura de Computadores AULA 9 CMPE382 Fall 2002 ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

("Strip Mining the Cache"). /* After */. for (jj = 0; jj < N; jj = jj+B) ... Dealing with aliases

(sometimes called **synonyms**); ...regulus.pcs.usp.br/~pcs5702/pdf/aula9e10.pdf[PDF] PCS 5702 – Arquitetura de Computadores AULA 6 CMPE382 Fall 2002 ...File Format: PDF/Adobe Acrobat - [View as HTML](#)("Strip Mining the Cache"). /* After */. for (jj = 0; jj < N; jj = jj+B) ... The **compiler** can

insert prefetch instructions to request the data ...

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The **compiler** automatically **strip-mines** your loop and generates a cleanup loop. ...**Strip Mining** and Cleanup Loops. Statements in the Loop Body ...www.tacc.utexas.edu/resources/userguides/intel/c_ug_inx.pdf[PDF] ARIZONA GAME AND FISH DEPARTMENT HERITAGE DATA MANAGEMENT SYSTEM ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

plant in clay soils, and may be of considerable value revegetating **mine** spoils and ...
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A **synonym** for androgyny; now little used, but it's in the ... The **compiler** of this Dictionary thinks of zirsself less as a com-. bination of male and female, ...
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At the time of writing, running the Pan **compiler** requires having the ... As a first example, Figure 7.1 shows an infinitely tall vertical **strip** of unit ...
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Synonyms: (British.) potholing; spelunking. 2. A method of mining in ... effects such as **strip mining**. recorder. An instrument designed to ...
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strip mining, as well as crop and livestock production, have impacted stream water ...
 Mills, MR **compiler**. 1988. Fish collection catalogue of the Kentucky ...
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The SRL **compiler** produces flow data files which NeMaC can read and download to a ...
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Current **strip-mining** technology practiced in West German brown coal fields is ...
 directly from the **mine** or plant to the final **compiler**. ...
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LOGSTDBY status: ORA-16111: log **mining** and apply setting up ... setting the **Strip**
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Strip mining can indirectly affect the coast due. to increased erosion, which increases
 sediment ... The term beach is sometimes used as a **synonym** for ...
www.usace.army.mil/usace-docs/eng-manuals/em1110-2-1810/entire.pdf

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The f95 **compiler** also provides a Fortran 77 compatibility mode that accepts most ...
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by **mining**. It has even been incorporated into poultry feed. Aqua- ... buffer **strip**,
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compiler into enzymes, which correspond, to the machine code. ... **synonyms**, the NLU unit constructs a frame-based semantic structure [36,37,38,39], which ... agents.media.mit.edu/projects/iseek/ashwani-ms.pdf

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gold **mine** of etymological information, thanks in part to the. eminent Yale philologist William Dwight ... **compiler** of the Dictionary of Underworld Slang ... www.verbatimmag.com/27_2.pdf

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Edges a little rubbed and chipped at the crowns; back **strip** on volume one repaired; ... Lanoe is an anagram for "alone," and Falconer a **synonym** of Hawker. ... www.brickrow.com/pdfs/OL7.pdf

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compiler. See the **strip** command in the AIX Commands Reference and the ... To compile myprogram.c using a substitute xlc **compiler** in /lib/tmp/mine/ enter: ... jumpdoc.fz-juelich.de/doc_vac_pdf/compiler.pdf

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Syn nyms:(British.) potholing;spelunking. 2. A method of **mining** in which the ore is ... effects such as **strip mining**. recorder. An instrument designed to ... www.epa.gov/ncea/pdfs/lexicon.pdf

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become a **mine** of information. It has got to the stage. that the board has had to impose a page ... reads in all the **strip** for the image, and appends them ... www.auug.org.au/publications/auugn/2002/auugn_23_2.pdf

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to the ... characterize using performance monitors (**cache** misses, ...
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know max length of app. is < max vector length, no **strip mining** overhead ...
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 Figure 2(c) expresses this traversal by applying **strip-mine**. and interchange to Figure
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mining. In **strip mining** an element of a vector register. handles every Lth-element ...
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scalar processors. Vectorization exploits regularities in the computational struc- ... ters
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Previous work has used unroll-and-jam for **scalar** replacement (replacing array memory ... The shorter inner loops of **strip-mining** can negatively impact ...
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through another half (**cache** line) of the L1 **cach** . The **scalar** product results are accumulated ... ant with partial pivoting and **strip-mining** is used [5,2] ...
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data locality – increase reuse of registers, **cache** ... **Strip Mining** is always safe. With interchange it ... **Scalar** replacement exposes the reuse of a(i) ...
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making assumptions have been made 100 **cache** hit rate, no constraints to ... limiting the number of elements in a structure, or by **strip-mining** the loop. ...

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improve register/**cache**/TLB locality due to. shorter code length ... **strip mining**. do i = 1, n. a(i) = b(i) * c(i). end do. do si = 1, n, 64 ...

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scalar operations on the same number of data ... **Strip mining**. V. When a vector has a length greater than that of the vector ...

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sizes are smaller than the **cache** size. This technique is known as **strip mining**. In the matrix multiplication context, this means that instead of 3 nested ...

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Memory. Vector. registers. Data. **cache**. **Scalar** FP. registers. **Scalar**. registers. Vload / VStore ... called **strip mining**. DO I = 1, N. A(I) = B(I) + C(I) ...

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tile or **strip-mining** rather than a full range of exploration on ... include one or several references to arrays (or **scalar**, since the ...

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global **scalar** variables (x and y) that can use the same. translation. ... **strip-mining** (a single loop is transformed into a loop. nest with two loops [15]), ...

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Strip-mine and Interchange. for (...jj +=N). for(...i++). for(j=jj;j<jj+N;j++) ... Memory operation B is **cache**-dependent on A if ...

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elsewhere in the code, by a **scalar** a thus reducing the size ... by the **cache**.

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Case Study 2: Blocking (or **Strip Mining**). Consider this blocked code (the inner loop should now fit in L1 **cache**):. DIMENSION A(N, N), B(N, N) ...

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Similar methods can be used to improve **cache**. performance. The key transformation. for this. purpose is **strip-mine-and-interchange**. [Por89] ...

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mor levels of **cache** to bridge the gap between proces-. sor and memory speeds, ...
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Message **Strip-Mining**. shared [] double *p;. float *buf;. get(buf,p,N*8);
for(i=0;i<N;i++) Computation cost: **cache** miss penalties + computation time ...
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Consider a memory system where the latency to fetch a 64-byte **cache** block is 256ns and the ... Use **strip mining** to. write DLXV code for the following loop. ...
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of distinct orderings of the loops in a nest in terms of **cache** lines used. ... ing **strip-mining**, this value is used to divide the iteration count of the ...
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Strip-mining is also used in order to reduce the amount of data relocated for the inner-loop. computation if that amount is too large to t in the **cache**. ...
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To further reduce the data **cache** misses, the **strip mining** optimization is applied to the. original code (figure3 (C)) by performing the computation on ...
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Optimization for **Cache** Memory. **Strip-mine** directive. **Strip-mine** directive. Array A.
*SOPTION STRIPMINING_START. DO J=1,N. DO I=L, N ... = A(I,J) ...
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***Not advantageous for machines w/**cache** ... mining. In **strip mining** an element of a vector register. handles very Lth-element ...
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that the **cache** miss penalty is 6 + Block size in words. The CPI for this workload ...
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ELL algorithm where "**strip-mining**" is employed to reuse vector registers. The ... back
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Avoid **cache** misses. Avoid **cache** misses. Reduce the working data set. Reduce the
working data set. Use **Strip**. Use **Strip--mining** to transform a large data ...
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Tiling is legal if and only if the **strip-mining** and loop permutation are legal.
Strip-mining. alone is always legal; the loop nest dimensionality is increased, ...
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direct approach, but the **strip** size may be used to deter-. **mine** the amount of data
loaded into the **cache** for each. array referenced in the inner loops. ...
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A vector of arbitrary length processed with **strip mining**. $m = n \bmod MVL$... Tiling is
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different options for **cache**/translation organization ... **strip mining**. • timing for vector
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Strip mining. • **Strip mining** can be extended over the whole code ! see domain
decomposition. But: Generally only a fraction of the **cach** (1/4) can be used ...
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Texture **cache**. Resident and non-resident textures. Z-Buffer write **cache**, state buffer
... **Strip-mine** vertex streams. Round-robin distribution of triangles ...

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The process is called **strip-mining**. The **strip-** ... **cache** sizes. Sweep accesses of a large vector may result in complete reloading of ...

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Cache memory can be used to decrease the number of. memory reference operations in ... performance reduces "**strip mining**" and other overhead costs. ...

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Cache Creek Mining Company extracts 100 tons of coal at. its **mine** in the northern Susitna ... Emile Usibelli and TE Sanford open a **strip mine** on Healy ...

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Triangular **Cache** Blocking. • Applying **strip mining**. DO I = 2, N, K. DO ii = I, I+K-1. DO J = 1, ii - 1. A(ii, J) = A(ii,ii) + A(J,J) ...

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Figure 2(c) expresses this traversal by applying **strip-mine** ... tive for memory parallelism, as the **cache** can see extra con- ...

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We prefer to use unroll-and-jam instead of **strip-mine** and interchange for two reasons. ... conservative for memory parallelism, as the **cache** can see extra ...

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unrolling, **cache** tiling/**strip-mining**, interchange etc.) on steroids, and is a big win for. every architecture but a huge win on IA64 due to its big caches ...

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we'll **cache** for another column). To someone untrained in acting ... **Strip-mining**, extracting the greatest amount of coal with ...

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Use performance model to **strip-mine** messages into ... 4 SSP slightly better than 1 MSP, 2 MSP much better than 8 SSP (**cache** ...

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data locality – increase reuse of registers, **cache**. • parallelism – eliminate loop-carried deps, incr. ... **Strip Mining** is always safe. With interchang it ...

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misses, and such overlap only occurs if read misses to multiple **cache** lines appear ...

The shorter inner loops of **strip-mining** can negatively impact ...

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No knowledge on communications, **cache**, physical mapping on processors, on

memory, etc. ... Consider loop coalescing and **strip mining**: DOPAR i=1, N ...

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improve register/**cache**/TLB locality due to. shorter code length ... **strip mining**. do i =

1, n. a(i) = b(i) * c(i). end do. do si = 1, n, 64 ...

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block) ... Use **strip mining** to minimize vector load overhead ...

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Mining. •. Message vectorization is the optimization with highest impact ...

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Bromley, RG, 1991: Zoophycos: **Strip mine**, refuse dump, **cache** or sewage farm?

Lethaia,. 24, 460-462. Bromley, RG, AA Ekdale, and U. Asgaard, 1999: Zoophycos ...

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one **cache** level resides on-chip. The transfer and storage organization is left ... loop

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